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JIAWEI HUANG	(Depositor's name)
	(Signature)
MARCH 25, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/758,919	01/16/2004	Chih-Feng Huang	JCLA12651	7053

TITLE OF INVENTION: ISOLATED HIGH-VOLTAGE LDMOS TRANSISTOR HAVING A SPLIT WELL STRUCTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$1000	05/24/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
PHAM, LONG	2814	257-356000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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SYSTEM GENERAL CORP.

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TAIPEI HSIEN, TAIWAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date 03/25/2005

Typed or printed name JIAWEI HUANG

Registration No. 43,330

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PTOL-85 (Rev. 12/04) Approved for use through 04/30/2007.

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